



US006504524B1

(12) **United States Patent**
Gates et al.

(10) **Patent No.: US 6,504,524 B1**
(45) **Date of Patent: Jan. 7, 2003**

(54) **ADDRESSING METHODS FOR DISPLAYS
HAVING ZERO TIME-AVERAGE FIELD**

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(*) Notice: Subject to any disclaimer, the term of this
patent is extended or adjusted under 35
U.S.C. 154(b) by 0 days.

(21) Appl. No.: **09/520,743**

(22) Filed: **Mar. 8, 2000**

(51) **Int. Cl.**⁷ **G09G 3/34**

(52) **U.S. Cl.** **345/107; 345/48; 345/84;**
345/210; 315/169.3; 349/86; 359/296; 359/297

(58) **Field of Search** **345/48, 84, 107,**
345/210; 315/169.3; 349/96; 359/296, 297

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(57) **ABSTRACT**

Novel addressing schemes for controlling electronically
addressable displays include the use of addressing signals
with additional signals having opposite polarity and equal
integrated signal strength and addressing schemes that mini-
mize the number of state changes that a display element
undergoes. In one embodiment, pre-pulses are employed to
apply a pre-stress to an display element that is equal and
opposite to the electrical stress applied in addressing the
element. In another embodiment, the addressing signal is
followed by a post-stressing pulse. Methods for minimizing
the number of display elements that must change state to
change the image displayed include the determination of a
set of elements that must be deactivated and a set of
elements that must be activated to change the image
depicted by a display.

38 Claims, 8 Drawing Sheets

